

Contact properties of high-mobility, air-stable, low-voltage organic n-channel thin-film transistors based on a naphthalene tetracarboxylic diimide

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Air-stable bottom-gate, top-contact n-channel organic transistors based on a naphthalene diimide exhibiting electron mobilities up to $0.8 \text{ cm}^2/\text{Vs}$ at low voltages were fabricated. Transistors with channel lengths of 1 μ m show a transconductance of 60 mS/m, but are significantly limited by the contact resistance. Transmission line measurements in combination with contact resistance models were applied to investigate this influence. Both contact resistance and contact resistivity are proportional to the inverse gate overdrive voltage. Organic complementary ring oscillators were fabricated on a flexible plastic substrate showing record signal delays down to 17 μ s at a supply voltage of 2.6 V. © 2013 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4811127]

Organic thin film transistors (TFTs) are useful for flexible, large-area electronics.^{1–5} For many of these applications, the TFTs should be able to operate at low voltages and have large field-effect mobility, negligible contact resistance, good air stability, and high switching speed. One possibility to increase the switching speed is to reduce the intrinsic and parasitic device capacitances by minimizing the lateral dimensions (channel length and overlap between gate electrode and source/drain contacts). However, at small dimensions, parasitic resistances at the source/drain contacts can become dominant, necessitating a careful analysis of the contact properties.

For organic p-channel TFTs, these requirements have been met with good results.^{6,7} To benefit from the advantages of complementary circuits (low static power dissipation, large noise margins), organic n-channel transistors with similar performance are needed as well.

The frequency limit of organic TFTs is usually extracted from the signal delay of ring oscillators. For organic complementary ring oscillators, stage delays below 1 μ s have been reported.^{8,9} However, these circuits required high voltages (≥ 20 V) and an inert atmosphere, due to the lack of air stability of the C₆₀ n-channel TFTs. Ambient operation of organic complementary circuits is possible with n-channel TFTs based on F₁₆CuPc¹⁰ that show excellent air stability,¹¹ but poor electron mobility (0.05 cm²/Vs).

Recently, air-stable n-channel TFTs based on highmobility naphthalene and perylene derivatives have been demonstrated.^{12–17} Here, n-channel TFTs based on N,N'-bis-(heptafluorobutyl)-2,6-dichloro-1,4,5,8-naphthalene tetracarboxylic diimide (NTCDI-Cl₂-(CH₂C₃F₇)₂) are investigated. High-voltage NTCDI-Cl₂-(CH₂C₃F₇)₂ TFTs already showed electron mobilities up to 1.3 cm²/Vs.¹⁷

Initially, organic TFTs were fabricated on doped Si substrates with a 30-nm-tick global Al back-gate. The gate dielectric consists of 3.6-nm-thick plasma-grown AlO_x and a 1.7-nm-thick self-assembled monolayer of n-tetradecylphosphonic acid, enabling low-voltage operation due to its high areal capacitance (800 nF/cm²).¹⁸ On the gate dielectric, nominally 20 nm of the organic semiconductor NTCDI-Cl₂-(CH₂C₃ F_7)₂ (Fig. 1(a)) were deposited by vacuum sublimation at a substrate temperature of 50 °C and a deposition rate of 0.3 Å/s. Figs. 1(b) and 1(c) show terraced layers with step heights of about 2 nm which is close to the length of the molecule,¹⁶ indicating that the molecules are standing approximately upright. Finally, 25nm-thick gold source/drain contacts were evaporated through high-resolution silicon stencil masks. All electrical measurements were performed in air at room temperature.

Fig. 2 shows the transfer (drain-source voltage $V_{DS} = 1.5$ V) and output characteristics (gate-source voltage V_{GS} up to 3 V) of a TFT with a channel width of $W = 50 \,\mu\text{m}$, a channel length of $L = 1 \,\mu\text{m}$, and a contact length (overlap between source/drain contact and gate electrode) of $L_C = 200 \,\mu\text{m}$. The TFT has a maximum effective field-effect mobility of 0.06 cm²/Vs, an on/off current ratio of 10⁶, a



FIG. 1. (a) Chemical structure of NTCDI-Cl₂-(CH₂C₃F₇)₂. (b) AFM image of a nominally 20-nm-thick vacuum-deposited NTCDI-Cl₂-(CH₂C₃F₇)₂ film. (c) Height profile of the section marked in Fig. 1(b).

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FIG. 2. Current-voltage characteristics of an organic n-channel TFT with a channel length of 1 μ m, measured under ambient conditions. (a) Transfer curve. (b) Output curve.

subthreshold swing of 180 mV/decade, a threshold voltage of 0.3 V, a transconductance of 60 mS/m, and minimal hysteresis. The small value of the effective mobility is due to the short channel length at which the contact resistance dominates over the channel resistance. In Fig. 3(a), the dependence of the effective mobility on the channel length in the linear regime is shown, demonstrating an increase up to 0.8 cm²/Vs for a channel length of 100 μ m.

From the simple equation,¹⁹ $\mu_{eff} = \mu_0 R_{ch}/(R_C + R_{ch})$ with the effective field-effect mobility μ_{eff} , the intrinsic field-effect mobility μ_0 (free of contact effects), the contact resistance R_C and the channel resistance R_{ch} , the channel-length dependence of the effective mobility can be formulated as

$$\mu_{eff} = \frac{\mu_0}{1 + \frac{L_{1/2}}{I}},\tag{1}$$

where $L_{1/2}$ is a characteristic channel length at which the contact resistance and the channel resistance are equal. Using μ_0 and $L_{1/2}$ as fitting parameters, the measured effective mobilities in Fig. 3(a) can be properly reproduced by Eq. (1). The fit gives an intrinsic mobility of 1.5 cm²/Vs and $L_{1/2} = 85 \ \mu\text{m}$.

A more general approach to determine the intrinsic transistor properties is the transmission line method $(TLM)^{20}$ which considers the total resistance of transistors with different channel lengths measured directly at a fixed overdrive voltage $V_{GS} - V_{th}$. In the linear regime, the total resistance R_{tot} can be expressed in terms of the channel length by

$$R_{tot}W = R_CW + \frac{L}{\mu_0 C_i (V_{GS} - V_{th})},\tag{2}$$

where C_i is the areal gate-dielectric capacitance and V_{th} is the threshold voltage, here extracted by the second derivative method.²¹ TLM is strictly valid only if the contact resistance is independent of V_{DS} . The slight nonlinearity seen in Fig. 2(b) at small V_{DS} indicates a small dependence of the contact resistance on V_{DS} , an observation that is supported by V_{DS} -dependent contact resistance measurements on a different sample (not shown). However, since this effect is very small, it can be safely ignored here. Plotting $R_{tot}W$ as a function of L, the width-normalized contact resistance $R_C W$ and the intrinsic mobility μ_0 can be extracted from the y-axis intercept and the slope of the linear fit, respectively. This is demonstrated in Fig. 3(b) for different gate overdrive voltages at $V_{DS} = 0.1$ V. For a gate overdrive voltage of 1.6 V, the fit provides a width-normalized contact resistance of $4.7 \text{ k}\Omega$ cm and an intrinsic electron mobility of 1.4 cm²/Vs. For high-performance p-channel TFTs fabricated with the same device architecture, but using dinaphtho[2,3-b:2',3'-f]thieno[3,2-b]thiophene (DNTT) as the semiconductor, values of $R_C W = 0.6 \text{ k}\Omega \text{ cm}$ and $\mu_0 = 3 \text{ cm}^2/\text{Vs}$ were obtained under similar measurement conditions.⁶ It can be seen that the performance of our n-channel TFTs approaches that of high-performance pchannel devices, even though the contact resistance is somewhat larger in the n-channel TFTs, probably due to the less favorable line-up of the energy levels usually found at the contact/semiconductor interfaces in organic n-channel



FIG. 3. (a) Effective field-effect mobility of TFTs with a contact length of $200 \,\mu\text{m}$ as a function of the channel length at $V_{DS} = 0.1$ V. The red line is a fit according to Eq. (1). (b) Width-normalized total resistance as a function of the channel length for different gate overdrive voltages. All linear fit curves meet at a single point which defines a characteristic length $l_0 = 64 \,\mu\text{m}$ and a characteristic widthnormalized resistance $R_{C0}W = 1 \,k\Omega$ cm.

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transistors compared with optimized p-channel TFTs.²² For DNTT (HOMO of -5.4 eV)²³ and NTCDI-Cl₂-(CH₂C₃F₇)₂ (LUMO of -4 eV),¹⁶ the nominal energy barriers are 0.4 eV and 1 eV assuming a work function of gold of around 5 eV.^{24,25} The real values are probably altered by effects like interface dipoles, surface contamination and diffusion of the contact metal into the semiconductor.^{26,27}

In staggered TFT architectures, the area contributing to charge injection must be considered carefully. The model commonly known as current crowding describes the contact regions as resistor networks where the effective injection area is the result of a balance between the vertical resistances (contact resistivity ρ_C) and the sheet resistance R_{sheet} of the channel.^{28,29} This defines the transfer length L_T , which is the effective length over which most of the charge carriers are exchanged between the contacts and the semiconductor,

$$L_T = \sqrt{\frac{\rho_C}{R_{sheet}}}.$$
 (3)

For long contact lengths ($L_C \gg L_T$), Berger²⁸ has shown that the dependence of the total resistance on the channel length can then be expressed as

$$R_{tot}W = \frac{L + 2L_T}{\mu_0 C_i (V_{GS} - V_{th})},\tag{4}$$

so the transfer length can be extracted by extrapolating the total resistance to the x-axis in Fig. 3(b). In addition, the contact resistivity ρ_C can be calculated by

$$\rho_C = (R_C W)^2 \mu_0 C_i (V_{GS} - V_{th}) / 4.$$
(5)

Fig. 4 shows the dependence of several extracted transistor parameters on the gate overdrive voltage. While the intrinsic mobility and the transfer length show only little variation with values of around $\mu_0 = 1.35 \text{ cm}^2/\text{Vs}$ and $L_T = 40 \,\mu\text{m}$, the contact resistance and contact resistivity both significantly decrease with increasing gate overdrive voltage. The observation that the contact resistance decreases with increasing gate overdrive voltage can be explained as follows: A higher gate overdrive voltage induces a greater density of accumulated charges in the channel and thereby decreases the sheet resistance which constitutes the horizontal part of the contact resistance. A reduced sheet resistance makes a greater contact area available for charge transfer and consequently reduces the contact resistance. This would be true even if the contact resistivity was not affected by the gate overdrive voltage. However, our measurements indicate that the contact resistivity, i.e., the vertical part of the contact resistance, also decreases with increasing gate overdrive voltage, even though there is no direct dependence between the contact resistivity and the sheet resistance. This observation can be explained as follows: A higher gate overdrive voltage induces not only a greater carrier density in the channel, but also a significant tail of charge carriers in the bulk of the semiconducting film, thereby reducing the bulk resistivity originating from the vertical charge transport through the semiconductor between the source/drain contacts and the channel.^{30,31} The large observed variation of the contact resistivity with the gate overdrive voltage indicates a significant contribution of the bulk resistivity if the resistivity of the contact-semiconductor interface is assumed to depend only weakly on the gate overdrive voltage.

In Fig. 3(b), the linear fits for the various gate overdrive voltages all cross at a single point. Based on this fact, Luan *et al.* proposed an empirical model where the width-normalized contact resistance is composed of a constant part $R_{C,0}$ W in series with an accumulation layer characterized by the length l_0 and the mobility μ ,³²

$$R_C W = R_{C,0} W + \frac{l_0}{\mu C_i (V_{GS} - V_{th})}.$$
 (6)

In order to illustrate that the curve indeed shows a linear dependence, we plot in Fig. 4(c) the contact resistance as a function of the inverse gate overdrive voltage. Using $l_0 = 64 \,\mu\text{m}$ and $R_{C,0}W = 1 \,\text{k}\Omega$ cm extracted from the intersection point in Fig. 3(b), the data can be properly fitted by Eq. (6) with $\mu = 1.33 \,\text{cm}^2/\text{Vs}$. Due to the charge accumulation in the bulk semiconductor,³⁰ the contact resistivity ρ_C also depends linearly on the inverse gate overdrive voltage, with the axis intercept $\rho_{C,0} = 4 \,\Omega \,\text{cm}^2$.

As shown in Fig. 5(a), the transistors have a good stability in air. The fact that the electron mobility was still above $0.3 \text{ cm}^2/\text{Vs}$ after these TFTs had been stored in a cleanroom under ambient conditions for about seven months probably originates from the low LUMO energy of NTCDI-Cl₂-(CH₂C₃F₇)₂ of about -4 eV (Ref. 16) that helps to resist ambient oxidation.³³ Another factor is the kinetic barrier created



FIG. 4. (a) Schematic of the source side of the TFT, illustrating the different resistances. (b) Intrinsic mobility (top) and transfer length (bottom) as a function of the gate overdrive voltage. (c) Width-normalized contact resistance (top) and contact resistivity (bottom) as a function of the inverse gate overdrive voltage. The red lines are linear fits.

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FIG. 5. (a) Evolution of TFT parameters during air exposure at a gate overdrive voltage of 1.6 V. Top: Intrinsic electron mobility. Bottom: Width-normalized contact resistance (black circles), contact resistivity (blue squares), and constant part of the contact resistivity $\rho_{C,0}$ (blue triangles). The errors are taken from the standard error of the linear fit of R_{tot} vs. L. (b) Mass spectrum of a NTCDI-Cl₂-(CH₂C₃F₇)₂ film after about 14 months in air. No oxidation products are visible. Inset: Magnification of the region where oxidation products would be expected.

by the dense packing of the fluorocarbon chains¹⁶ in combination with the hydrophobicity of the semiconducting layer (contact angle 126) that hinder the penetration of water molecules into the channel. While the intrinsic mobility was still about one quarter of the initial value after seven months in air, the contact resistance had increased by a factor of around five and the contact resistivity and its constant part ρ_{C0} had increased by a factor of seven. This is surprising, since from a degradation of the intrinsic semiconductor mobility alone, the contact resistance and contact resistivity would be expected to increase with at most the same rate, even if the mobility in the semiconductor regions below (and protected by) the metal contacts degrades with the same rate as in the (unprotected) channel region. The fact that the increase in contact resistance and resistivity is slightly faster than the drop of the intrinsic mobility indicates that there is an additional degradation path for the contact resistance.

The nature of the degradation mechanism is unclear. For pentacene, De Angelis et al. have observed oxidation products by mass spectroscopy at additional masses of +15 amu and +16 amu (and multiples) that are believed to cause the observed mobility degradation in that material by producing trap states.³⁴ However, mass spectra of a thin NTCDI-Cl₂- $(CH_2C_3F_7)_2$ film taken after about 14 months in air using laser desorption ionization time-of-flight mass spectrometry (LDI-TOF-MS) show only negligible intensity in the mass range where oxidation products would be expected (Fig. 5(b)), so the observed reduction of the mobility does not seem to be caused by oxidation of the semiconductor molecules.

All electrical measurements shown so far were performed on the same sample. The statistics of fresh devices



FIG. 6. (a) Statistics of the width-normalized contact resistance extracted from 8 samples fabricated under the same conditions. (b) Statistics of the intrinsic mobility for 8 samples. The mean value is (0.86 ± 0.23) cm²/Vs.

obtained together with TFTs of seven other samples fabricated in the same way over a period of more than four months are presented in Fig. 6. For unknown reasons, three of the samples show a somewhat higher contact resistance, but most samples have a low contact resistance below $10 k\Omega$ cm. The mean intrinsic mobility is (0.85 ± 0.27) cm²/Vs.

In order to evaluate the dynamic performance of the TFTs, we fabricated 11-stage organic complementary ring oscillators based on NTCDI-Cl2-(CH2C3F7)2 (n-channel) and DNTT (p-channel).¹⁸ In contrast to the devices described above, the ring oscillators were fabricated on a flexible polyethylene naphthalate (PEN) substrate with local Al gate electrodes and Au interconnects, using in total five different stencil masks to pattern the various layers.

The signal propagation delay per stage τ was calculated as $\tau = 1/2fn$ with the measured oscillation frequency of the ring oscillators f and the number of stages n = 11. Fig. 7(a) shows τ as a function of the supply voltage V_{DD} for three different ring oscillators that are comprised of TFTs with channel lengths of 4, 2, and 1 μ m and contact lengths of 20, 10, and 5 μ m. In order to account for the difference in the effective hole and electron mobilities, the channel width of the nchannel TFTs ($W = 80 \,\mu m$) was chosen to be larger than that of the higher-mobility p-channel TFTs ($W = 40 \ \mu m$).

As the channel and contact lengths are reduced, the parasitic capacitances decrease, resulting in shorter propagation delays. As expected,³⁵ the frequency of the ring oscillators is proportional to the supply voltage (inset of Fig. 7(a)).



FIG. 7. (a) Stage delay of flexible 11-stage organic complementary ring oscillators based on NTCDI-Cl2-(CH2C3F7)2 (n-channel) and DNTT (pchannel) as a function of the supply voltage. The lines are guides to the eye. Inset: Oscillation frequency of the ring oscillators as a function of the supply voltage. The straight lines illustrate the linear relationship. (b) Output signal of the ring oscillator with a channel length of $1 \,\mu m$ and a contact length of $5\,\mu m$ at a supply voltage of 2.6 V. Inset: Photograph of the complementary ring oscillator.

For a supply voltage of 2.6 V, the output signal of the ring oscillator with a channel length of 1 μ m and a contact length of 5 μ m is shown in Fig. 7(b). The signal delay per stage is 17 μ s, which is the lowest value reported for organic complementary ring oscillators at low supply voltages (<10 V). For supply voltages above 10 V, the shortest signal delays of flexible organic complementary ring oscillators were reported by Smaal *et al.*³⁶ (10 μ s at 20 V) and by Baeg *et al.*³⁵ (1.25 μ s at 30 V).

In summary, we have presented n-channel organic thinfilm transistors with low-voltage operation, high mobility, small contact resistance and air stability. TFTs with channel lengths down to $1 \,\mu m$ have been fabricated, revealing a strong influence of the contact resistance. A detailed analysis of the contact resistance shows good agreement with existing models and significant contributions of the bulk semiconductor resistivity. Although the value of the contact resistance is relatively low and approaching that of p-channel transistors, it still severely limits the device performance at small channel lengths. Organic complementary ring oscillators on a flexible substrate with different channel and contact lengths were fabricated. A signal delay per stage as low as 17 μ s was measured, which is the lowest reported value for an organic complementary ring oscillator at supply voltages below 10 V.

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