COMMUNICATION

Mixed Self-Assembled Monolayer Gate Dielectrics for Continuous Threshold Voltage Control in Organic Transistors and Circuits

By *Ute Zschieschang,* Frederik Ante, Matthias Schlörholz, Maike Schmidt, Klaus Kern,* and *Hagen Klauk*

Organic thin-film transistors (TFTs) are potentially useful for flexible displays,[1] conformable sensor arrays,[2] and analog or digital integrated circuits.[3,4] Especially for digital circuits it is important that the threshold voltage of the transistors can be set to a specific value during transistor fabrication. The reason is that the threshold voltage of the transistors determines the input voltage at which the circuits switch between the two logic states ("0" and "1").[5,6] Ideally, this switching voltage is exactly half the circuit's supply voltage, since this provides the maximum voltage difference between the switching voltage and both the ground potential and the supply voltage potential, which in turn provides the maximum immunity of the circuits against the influence of electrical noise that is always present in electronic systems.[5,6] If the threshold voltage of the transistors cannot be controlled during manufacturing, the switching voltage may be too close to either the ground potential or the supply voltage potential, in which case the circuits may not work reliably (i.e., switch inadvertently) when exposed to electrical noise. Therefore, it is highly desirable that the threshold voltage of the transistors can be set to a specific value determined by the circuit designer.

Previously described methods to control the threshold voltage of organic TFTs include the use of a corona discharge, a low-energy electron beam, or a large electric field to change the amount of charge or polarization within the polymer gate dielectric layer,[7–10] the use of ultraviolet radiation to change the electronic properties of the semiconductor/dielectric interface,[11] and the functionalization of the SiO_2 gate dielectric with a self–assembled monolayer (SAM) of a silane with more or less electronegative substituents.[12,13] Here we demonstrate

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a novel approach that allows the threshold voltage of low–voltage (≤3 V) organic p-channel and n-channel TFTs and the switching voltage and noise margin of low-power organic complementary circuits to be tuned continuously over a wide range during manufacturing. Continuous threshold voltage tuning is facilitated by employing gate dielectrics that are based on mixed alkyl/fluoroalkyl phosphonic acid SAMs prepared by a simple solution process. The threshold voltage of the TFTs is a linear function of the atomic fluorine concentration in the mixed SAM dielectric, with a modulation coefficient of 40 mV/%.

The transistors and circuits are fabricated on glass substrates (see **Figure 1a**). Patterned gate electrodes are defined by depositing 30 nm thick aluminum by vacuum evaporation through a shadow mask, followed by a brief oxygen plasma that increases the thickness of the native aluminum oxide (AlO_x) layer from ~1.5 nm to 3.6 nm. The substrate is then immersed in a mixture of octadecylphosphonic acid (Figure 1b) and 12,12,13,13,14,14,15,15,16,16,17,17,18,18,18-pentadecylfluoro-octadecylphosphonic acid (Figure 1c) in 2-propanol, allowing a mixed organic monolayer to self-assemble on the oxidized aluminum. The total thickness of the AlO_x/SAM gate dielectric is 5.7 nm.[14–18] The conjugated organic semiconductors pentacene (for p-channel TFTs) and hexadecafluorocopperphthalocyanine ($F_{16}CuPc$, for n-channel TFTs) are then deposited in vacuum through shadow masks. TFTs and circuits are completed by evaporating gold source/drain contacts through another shadow mask. The highest process temperature is 90 °C (the substrate temperature during the $F_{16}CuPc$ deposition), which is fully compatible with flexible polymeric substrates.[14] All electrical measurements are performed at room temperature in air.

Compared with alkyl SAMs, the electronegative substituents in fluoroalkyl SAMs reduce the electron density in the carrier channel of the transistors, causing a change in threshold voltage towards more positive values. This was first demonstrated by Kobayashi et al. and Pernstich et al. for organic TFTs prepared on silicon substrates with thick, thermally grown SiO_2 gate dielectrics functionalized with various silane SAMs.[12,13] Kobayashi and Pernstich showed that the threshold voltage changes by a certain amount when the SiO_2 is covered with a SAM of a fluoroalkyl silane, rather than an alkyl silane. In contrast to the single-component SAMs employed by Kobayashi and Pernstich, we have prepared mixed SAMs of alkyl and fluoroalkyl molecules in order to tune the threshold voltage continuously, rather than discretely, by adjusting the mixing ratio of the two molecules. Having continuous control over the threshold voltage
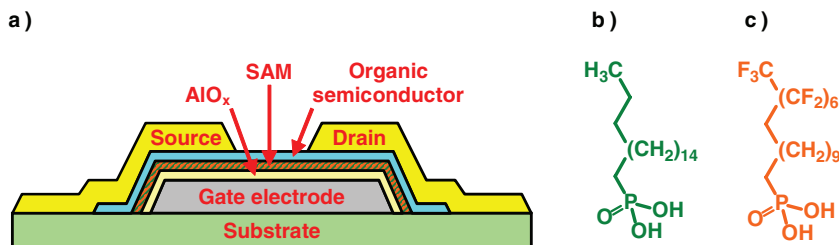
Figure 1. a) Schematic cross-section of the thin–film transistor (TFT) structure. The gate dielectric is a combination of a 3.6 nm thick, plasma-grown aluminum oxide layer and a 2.1 nm thick alkyl/fluoroalkyl phosphonic acid self-assembled monolayer. b) Chemical structure of octadecylphosphonic acid. c) Chemical structure of pentadecylfluoro–octadecylphosphonic acid.

makes it possible to place the switching voltage of the circuits at precisely half the supply voltage, producing the maximum noise margin. While Kobayashi and Pernstich employed silanes to form SAMs on high-temperature-grown SiO_2 dielectrics, we have utilized phosphonic acids to form high-quality monolayers on patterned Al gates covered with a thin AlO_x layer grown at room temperature.

Mixed SAMs have been studied by the Whitesides group[19] and by the groups of Allara and Weiss.[20] Bain et al. demonstrated that mixed SAMs are easily prepared by co-adsorbing aliphatic molecules with different tail groups from a mixed solution.[19] When Bain et al. compared the composition of the mixed SAMs (inferred from X-ray photoelectron spectroscopy (XPS) measurements) to the relative concentrations of the molecules in the adsorption solution they found that generally the ratio of the concentrations of the two components in the SAM is not the same as in solution, "but reflects the relative solubilities of the components in solution and interactions between the tail groups in the monolayers."[19] Interestingly, for a two-component system of methyl- and halogen-terminated thiols with the same chain length Bain et al. observed a linear relationship between the concentration in solution and the composition of the SAM.[19] This implies that the composition of mixed alkyl/fluoroalkyl SAMs can be tuned in a deterministic manner by

adjusting the mixing ratio in the solution, as long as the alkyl and fluoroalkyl components have the same chain length. Therefore, both the alkyl and fluoroalkyl molecules utilized in this work have the same chain length (18 carbon atoms). In principle, chain lengths shorter than 18 carbon atoms are also possible, although the gate leakage currents tend to be larger for shorter chain lengths.[16,17]

From contact-angle analysis Bain et al. further concluded that "multicomponent monolayers do not segregate into discrete single-component domains. Any islands that do form are too small to influence the contact angle by distorting the drop edge, placing an upper bound of about 0.1 μm on the size of any such islands."[19] This result was later challenged by Stranick et al. who investigated the structure of mixed SAMs with nanometer resolution by scanning tunneling microscopy (STM). They discovered that the molecules systematically phase separate into discrete domains.[20] However, the size of the molecular domains was less than 10 nm, i.e., several orders of magnitude smaller than the dimensions of thin-film transistors (usually >1 μm). This suggests that mixed SAMs lead to an effective electrostatic potential and that the impact of the different SAM molecules on the electron density in the semiconductor averages across the transistor dimensions, giving a uniform, reproducible effect on the threshold voltage.

We have measured water contact angles of 108° on octadecylphosphonic acid SAMs and 118° on pentadecylfluoro-octadecylphosphonic acid SAMs. For mixed SAMs, contact angles between 108° and 118° would therefore be expected. Indeed, SAMs prepared from a solution containing equal amounts of alkyl and fluoroalkyl phosphonic acid gave a contact angle of 114° (see **Figure 2** and **Figure 3a–c**). Water contact angles measured on the same monolayer vary by less than 1° (see Figure S1, Supporting Information), indicating that the phase separation of the alkyl and fluoroalkyl molecules in the SAMs averages over microscopic areas, as suggested by Bain et al.[19] The SAM composition has no detectable systematic effect on the dielectric capacitance and on the leakage current density measured on $Al/AlO_x/SAM/Au$ capacitors (see Figure S2, Supporting Information), confirming the high quality of the SAMs regardless of SAM composition. Analysis of the contact angles measured with water and with a non-polar liquid (n-heptane) using the Owen–Wendt method[21] shows that the surface energy of the SAM decreases from 20 mN/m to 12 mN/m as the fluoroalkyl content is increased from 0% to 100% (see Figure S3, Supporting Information).

The relationship between the mixing ratio in solution and the SAM composition is not linear. XPS measurements indicate that the concentration of fluoroalkyl phosphonic acid in the SAM approaches 100% even if the adsorption solution contains only 75% fluoroalkyl phosphonic acid (see Figure 2). This suggests that the adsorption of the fluoroalkyl phosphonic acid is favored over that of the alkyl phosphonic acid when the fluoroalkyl content in the solution exceeds 50%.

Figure 3d–f show the transfer characteristics of pentacene p-channel and $F_{16}CuPc$ n-channel TFTs prepared with gate
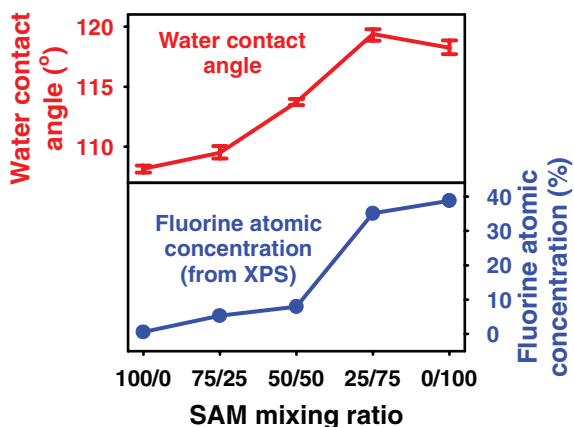

Figure 2. Relationship between the mixing ratio of alkyl and fluoroalkyl phosphonic acid molecules in the adsorption solution during SAM formation and the water contact angle (measured on the surface of the SAMs) and the atomic concentration of fluorine in the SAM-based gate dielectric (measured by X-ray photoelectron spectroscopy, XPS).
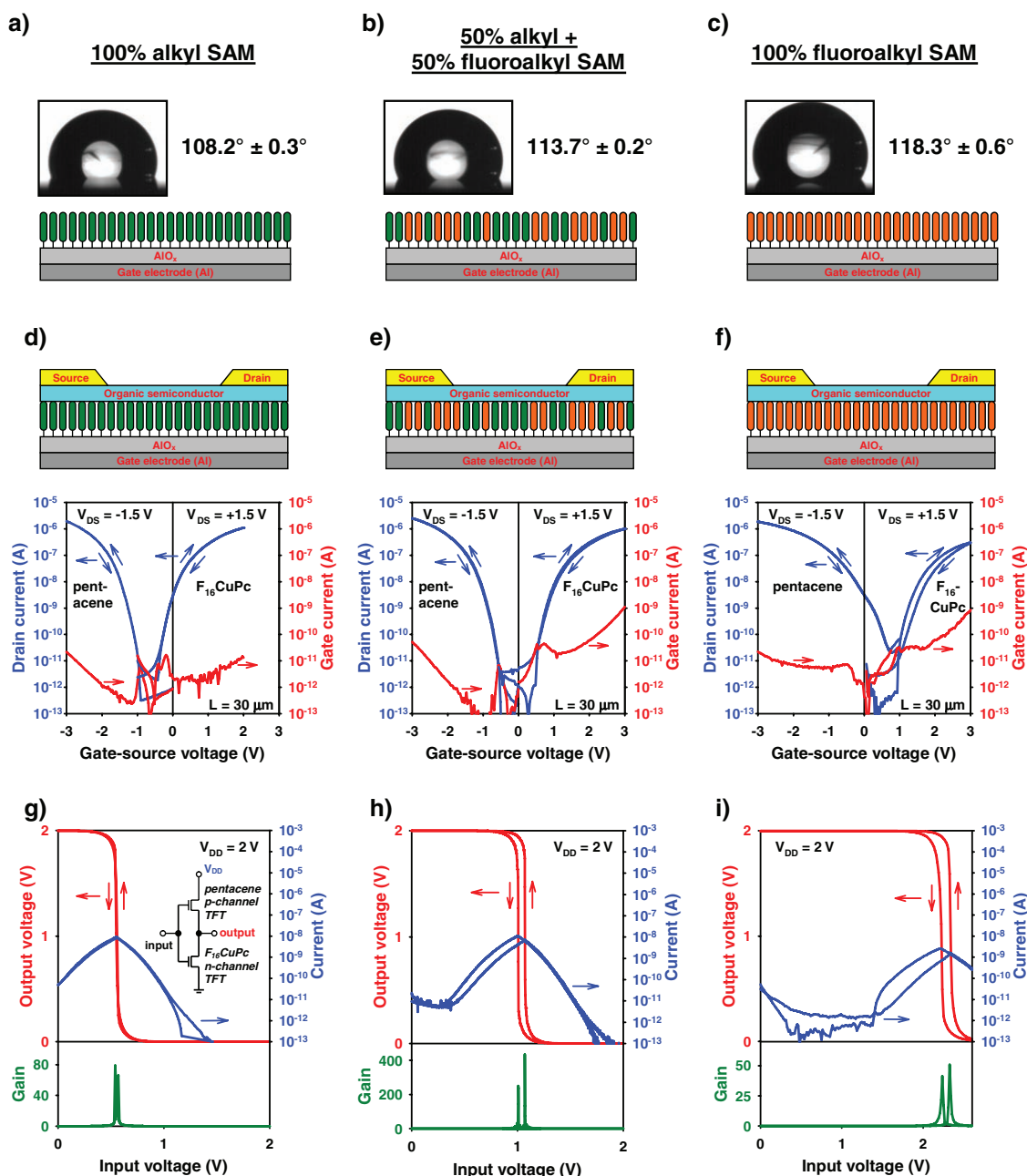
Figure 3. a–c) Water contact angles measured on a self-assembled monolayer (SAM) of 100% alkyl phosphonic acid (a), on a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid (b), and on a SAM of 100% fluoroalkyl phosphonic acid (c). d–f) Schematic cross-sections and transfer characteristics of pentacene p-channel TFTs and $F_{16}CuPc$ n-channel TFTs with three different SAMs as the gate dielectric: SAM of 100% alkyl phosphonic acid, mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid, and SAM of 100% fluoroalkyl phosphonic acid. g–h) Transfer characteristics of complementary inverters (each composed on one pentacene and one $F_{16}CuPc$ TFT) with the same SAMs. Using the mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid, the inverter has a switching voltage of exactly half the supply voltage (1 V) and a low power consumption of 20 pW (the product of the supply voltage, 2 V, and the maximum supply current, 10 pA).

dielectrics of 100% alkyl, 50% alkyl + 50% fluoroalkyl, and 100% fluoroalkyl SAMs. Due to the small thickness and large capacitance of the AlO_x/SAM gate dielectrics, all transistors operate with low voltages (3 V). The gate currents are no greater than 10^{-9} A, confirming the formation of dense monolayers for all SAM compositions. Atomic force microscopy (AFM) images of the semiconductor films deposited onto the SAMs show no

significant differences in film growth depending on the SAM composition (see Figure S4, Supporting Information).

Figure 3 shows that TFTs with 100% alkyl SAM dielectrics have negative threshold voltages (–1.1 V for pentacene TFTs, –0.3 V for $F_{16}CuPc$ TFTs), while TFTs with 100% fluoroalkyl SAM dielectrics have positive threshold voltages (0.5 V for pentacene TFTs, 1.1 V for $F_{16}CuPc$ TFTs). The difference in

threshold voltage between 100% alkyl and 100% fluoroalkyl SAMs is about 1.5 V, which is about half the maximum supply voltage. Inside this "window," the threshold voltage can be tuned continuously to any desired value by adjusting the mixing ratio in the adsorption solution. We found that threshold voltages that are symmetric with respect to $V_{GS} = 0$ are obtained for a mixing ratio of 50/50 (see Figure 3e and Figure S5, and Table S1, Supporting Information).

Figure 3g–i show the transfer characteristics of complementary inverters based on the pentacene p–channel and $F_{16}CuPc$ n–channel TFTs from Figure 3d–f. The effect of the SAM composition on the inverter switching voltage is clearly seen. When both TFT threshold voltages are negative (100% alkyl SAM), the inverter switching voltage is very close to the ground potential, so the inverter is very susceptible to electrical noise. When both threshold voltages are positive (100% fluoroalkyl SAM), the switching voltage is actually greater than the supply voltage, so the inverter cannot switch unless the input voltage exceeds the supply voltage. By selecting a mixing ratio that provides symmetric TFT threshold voltages (50% alkyl + 50% fluoroalkyl SAM), an inverter switching voltage of exactly half the supply voltage (1 V) is obtained (see Figure 3h).

One of the benefits of placing the switching voltage at half the supply voltage is greater noise immunity: Even if noise were to cause the input voltage to deviate by close to half the supply voltage in either direction, the inverter would still produce the correct output signal. A circuit's immunity against electrical noise is quantified as the noise margin: The inverter in Figure 3h (50% alkyl + 50% fluoroalkyl SAM) has a noise margin of 0.8 V (80% of ½V_{DD}; see Figure S6, Supporting Information); this is the largest noise margin reported so far for an organic circuit.[22]

A second important benefit of a symmetric switching voltage is that the circuits can be operated with lower supply voltages. This can be seen in **Figure 4** where the signal delay of two complementary ring oscillators is plotted as a function of the supply voltage. The ring oscillator that uses TFTs with a 100% alkyl SAM requires a minimum supply voltage of
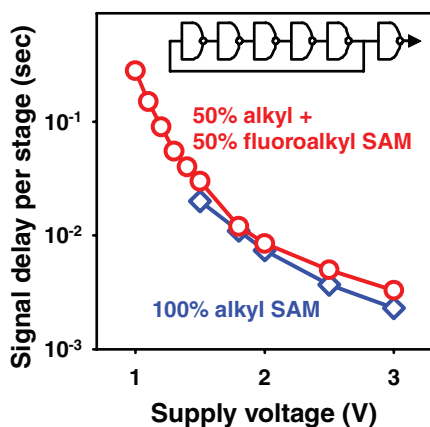

Figure 5. Relationship between the fluorine atomic concentration in the SAM gate dielectric (measured by XPS) and the threshold voltages of the p–channel and n–channel TFTs and the switching voltage of the complementary inverters.

1.5 V, while the ring oscillator with the mixed SAM operates with supply voltages as low as 1.0 V, which is the smallest supply voltage reported for organic complementary circuits to date.

Despite the fact that the relationship between the mixing ratio in solution and the SAM composition is not linear (as reported by Bain et al.[19] and as seen in Figure 2), the exact threshold voltages and switching voltages of TFTs and circuits with mixed SAM dielectrics can nonetheless be accurately predicted after calibration. **Figure 5** shows that the relationship between the threshold voltage and the atomic concentration of fluorine in the SAM-based gate dielectric (as determined by XPS) is indeed linear, as one would expect based on electrostatic considerations. Quantitatively, the change in threshold voltage induced by a change in atomic fluorine concentration is about 40 mV/%. These results demonstrate that ultra-thin dielectrics based on mixed organic monolayers provide a powerful method to continuously tune the threshold voltage of organic transistors in a deterministic and reproducible manner.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

Acknowledgements

Figure 4. Signal delay per stage as a function of supply voltage for two five–stage organic complementary ring oscillators: One is based on TFTs with a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid as the gate dielectric (red curve), the other is based on TFTs with a 100% alkyl phosphonic acid SAM dielectric (blue curve). The ring oscillator with the optimized mixed SAM gate dielectric operates with a supply voltage as low as 1 V. All TFTs have a channel length of 20 μm.
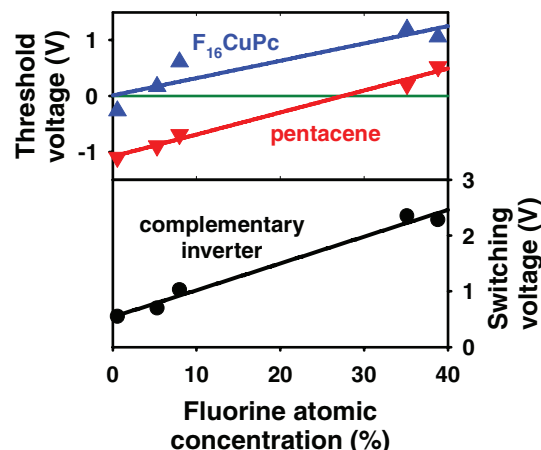
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Mixed self-assembled monolayer gate dielectrics for continuous threshold voltage control in organic transistors and circuits

By *Ute Zschieschang, Frederik Ante, Matthias Schlörholz, Maike Schmidt, Klaus Kern, and Hagen Klauk*

Figure S1. Variation of the water contact angle measured on a self-assembled monolayer (SAM) of 100% alkyl phosphonic acid (left), on a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid (center), and on a SAM of 100% fluoroalkyl phosphonic acid (right). Contact angles measured on the same monolayer vary by less than 1°, indicating that the phase separation of the alkyl and fluoroalkyl molecules averages over microscopic areas.
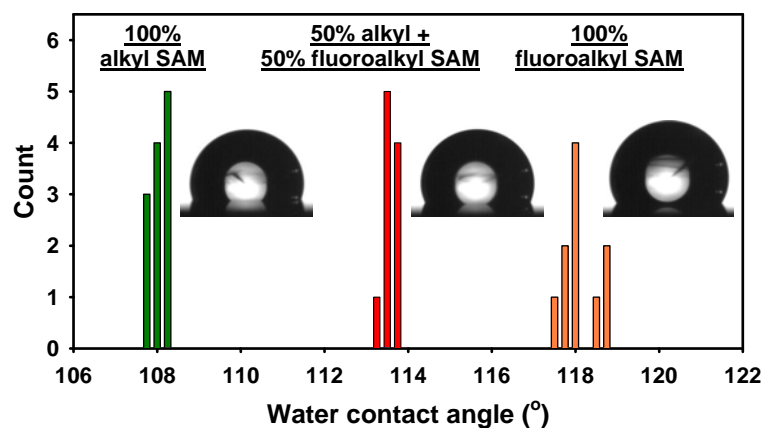
Figure S2. Capacitance and leakage current density were measured on Al / AlO$_x$ / SAM / Au test structures with shadow-mask-patterned Al bottom and Au top electrodes. The overlap area is 200 µm x 200 µm. The leakage current density measurements were repeated on 8 to 12 devices of each SAM composition. As can be seen, the SAM composition has no systematic effect on the capacitance and the leakage current density, confirming the high quality of the SAMs regardless of the SAM composition.

Figure S3. Owens-Wendth plot for a SAM of 100% alkyl phosphonic acid, a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid, and a SAM of 100% fluoroalkyl phosphonic acid. The data were obtained from contact-angle measureme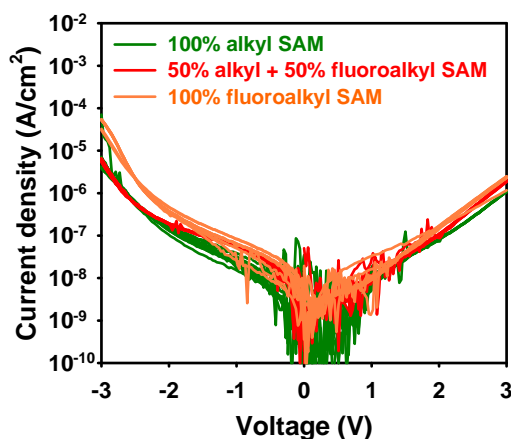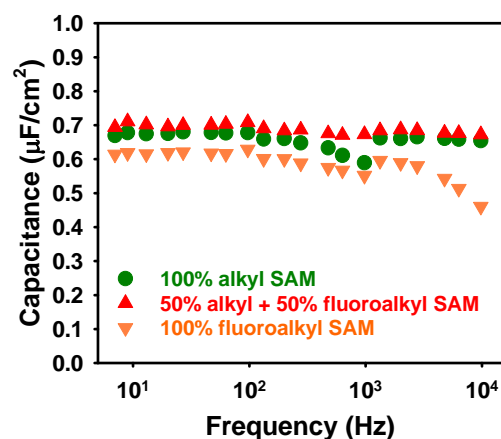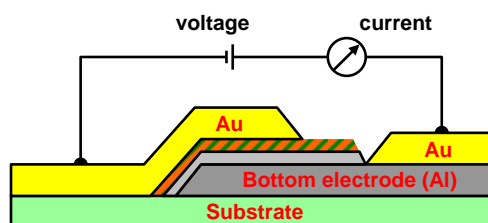nts performed with deionized water (right data points; γ^D = 26.4 mN/m, γ^P = 46.4 mN/m) and with *n*-heptane (left data points; γ^D = 19.9 mN/m, γ^P = 0 mN/m). The linear fits produce the following results for the polar and dispersive components of the surface energy of the SAMs:

100% alkyl SAM: γ^D = 19.9 mN/m, γ^P = 0.11 mN/m;
50% alkyl + 50% fluoroalkyl: γ^D = 17.3 mN/m, γ^P = 0.01 mN/m;
100% fluoroalkyl SAM: γ^D = 11.9 mN/m, γ^P = 0.05 mN/m.

| 100% alkyl SAM | 50% alkyl + 50% fluoroalkyl SAM | 100% fluoroalkyl SAM |

topography
$10 \times 10 \ \mu m^2$

topography
$10 \times 10 \ \mu m^2$

topography
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amplitude

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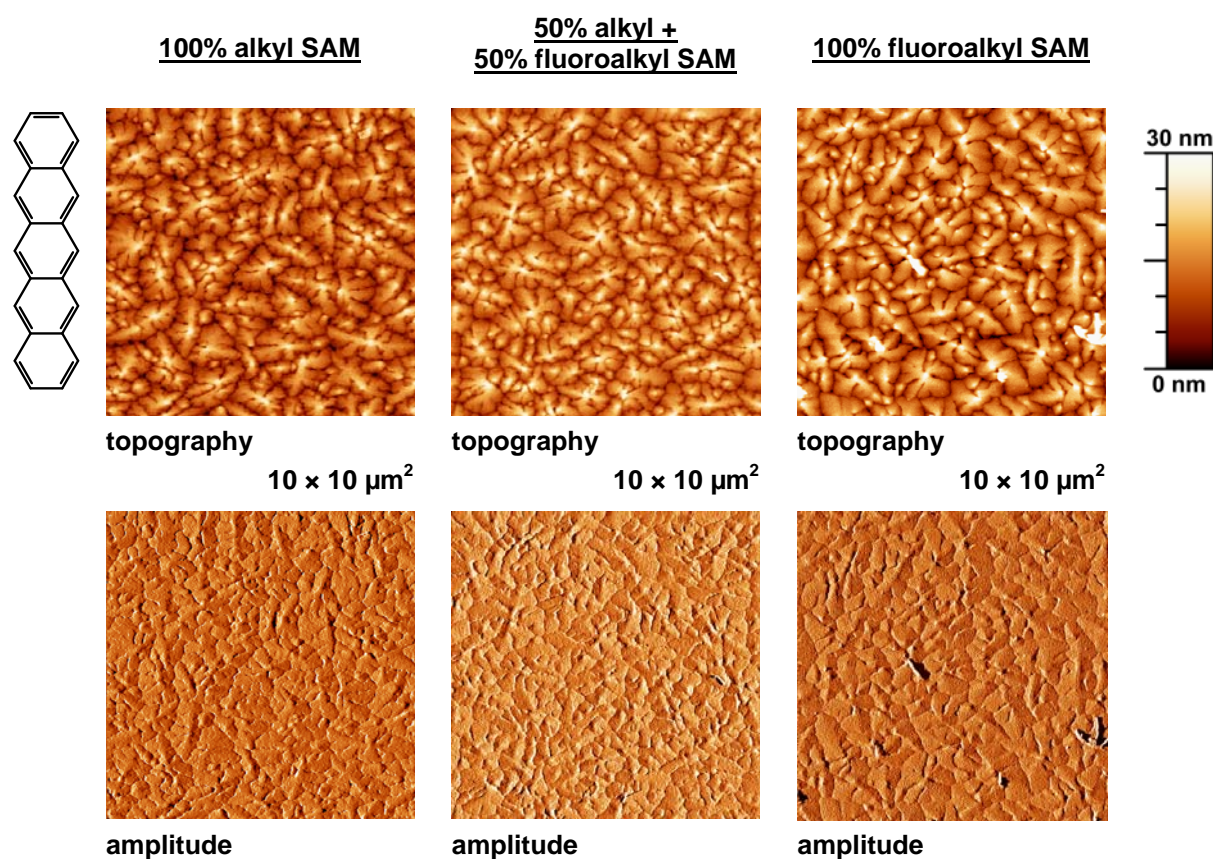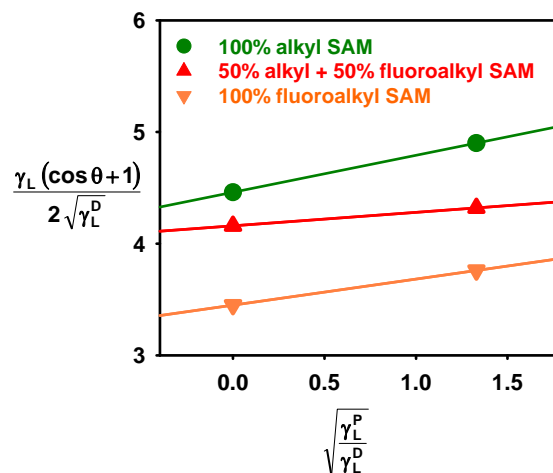
amplitude

Figure S4. Semiconductor thin-film morphology. Atomic force microscopy (AFM) topography and amplitude images of 30 nm thick pentacene films deposited by vacuum sublimation onto a self assembled monolayer (SAM) of 100% alkyl phosphonic acid (left), onto a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid (center), and onto a SAM of 100% fluoroalkyl phosphonic acid (right). Each image has a size of 10 µm × 10 µm. No systematic effect of the SAM composition on the morphology of the organic semiconductor film is observed.
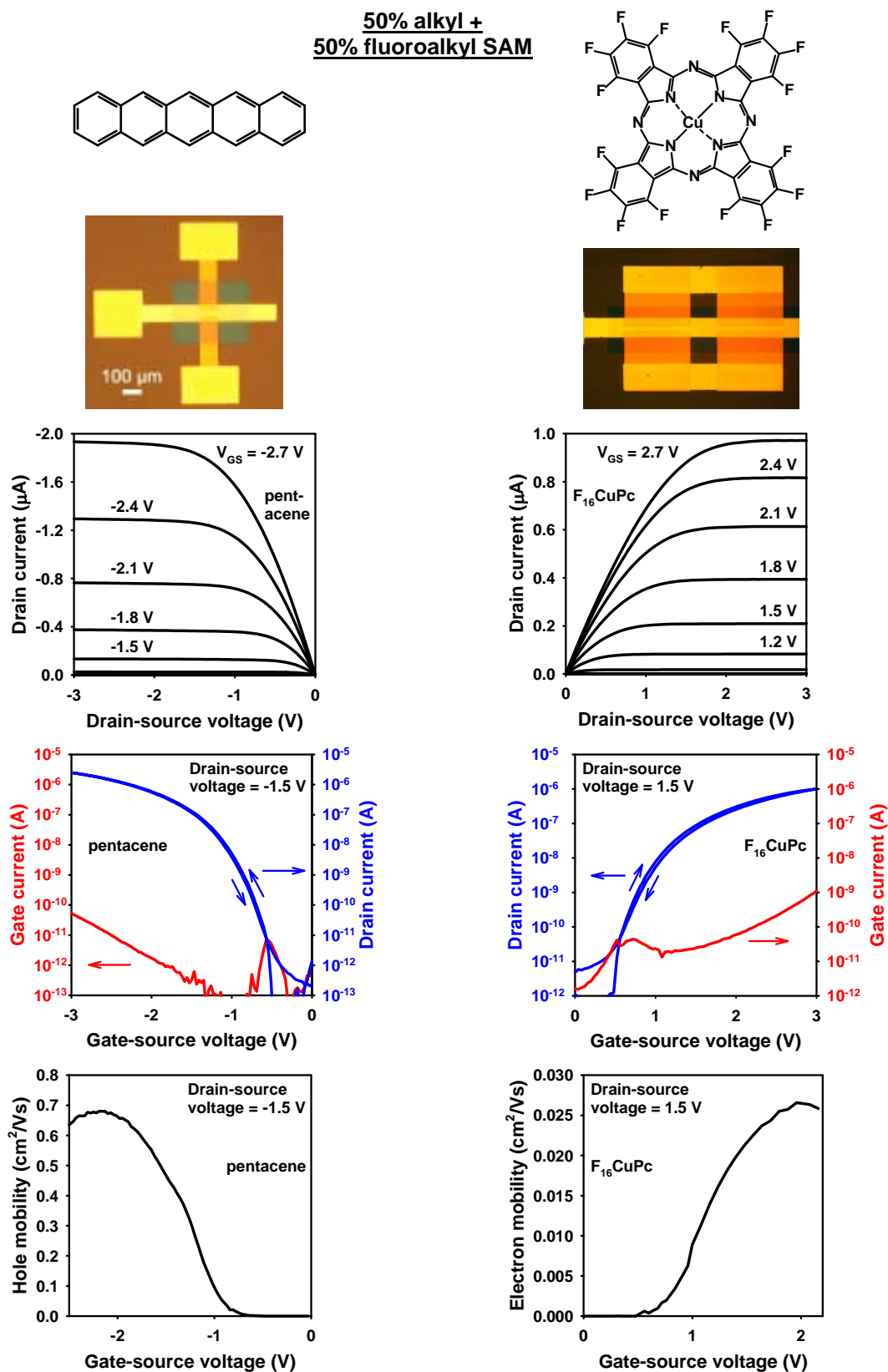
Figure S5. Transistor characteristics. Photographs and electrical characteristics of a pentacene p-channel TFT with a channel length of 30 µm and a channel width of 100 µm (left) and of a $F_{16}CuPc$ n-channel TFT with a channel length of 30 µm and a channel width of 1000 µm (right). For the optimum SAM gate dielectric composition (50% alkyl phosphonic acid + 50% fluoroalkyl phosphonic acid), the threshold voltages of the TFTs are symmetric with respect to $V_{GS} = 0$ V.

100% alkyl SAM

50% alkyl + 50% fluoroalkyl SAM

100% fluoroalkyl SAM

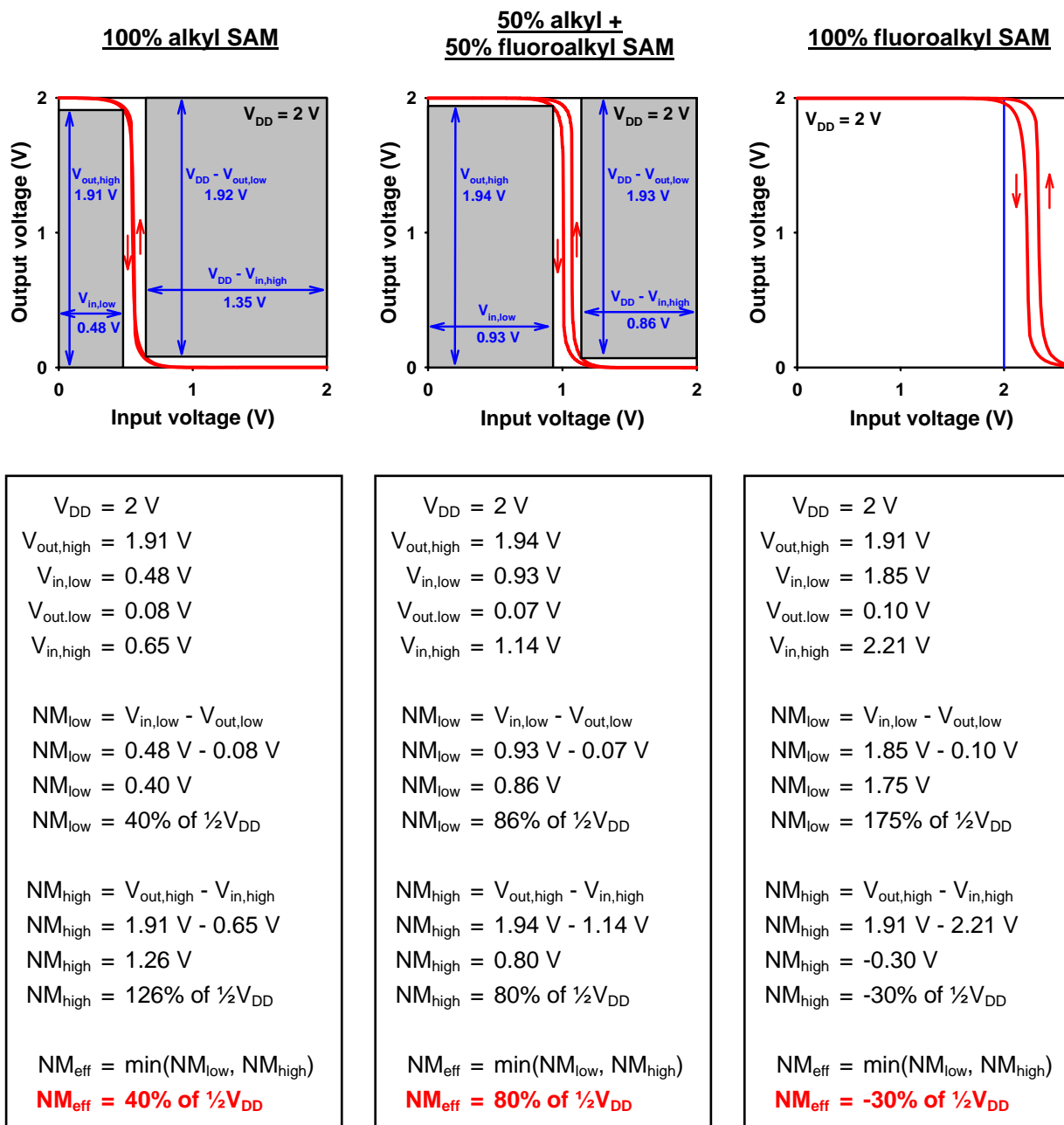| V_{DD} = 2 V	V_{DD} = 2 V	V_{DD} = 2 V
$V_{out,high}$ = 1.91 V	$V_{out,high}$ = 1.94 V	$V_{out,high}$ = 1.91 V
$V_{in,low}$ = 0.48 V	$V_{in,low}$ = 0.93 V	$V_{in,low}$ = 1.85 V
$V_{out.low}$ = 0.08 V	$V_{out.low}$ = 0.07 V	$V_{out.low}$ = 0.10 V
$V_{in,high}$ = 0.65 V	$V_{in,high}$ = 1.14 V	$V_{in,high}$ = 2.21 V
NM_{low} = $V_{in,low}$ - $V_{out,low}$	NM_{low} = $V_{in,low}$ - $V_{out,low}$	NM_{low} = $V_{in,low}$ - $V_{out,low}$
NM_{low} = 0.48 V - 0.08 V	NM_{low} = 0.93 V - 0.07 V	NM_{low} = 1.85 V - 0.10 V
NM_{low} = 0.40 V	NM_{low} = 0.86 V	NM_{low} = 1.75 V
NM_{low} = 40% of ½V_{DD}	NM_{low} = 86% of ½V_{DD}	NM_{low} = 175% of ½V_{DD}
NM_{high} = $V_{out,high}$ - $V_{in,high}$	NM_{high} = $V_{out,high}$ - $V_{in,high}$	NM_{high} = $V_{out,high}$ - $V_{in,high}$
NM_{high} = 1.91 V - 0.65 V	NM_{high} = 1.94 V - 1.14 V	NM_{high} = 1.91 V - 2.21 V
NM_{high} = 1.26 V	NM_{high} = 0.80 V	NM_{high} = -0.30 V
NM_{high} = 126% of ½V_{DD}	NM_{high} = 80% of ½V_{DD}	NM_{high} = -30% of ½V_{DD}
NM_{eff} = min(NM_{low}, NM_{high})	NM_{eff} = min(NM_{low}, NM_{high})	NM_{eff} = min(NM_{low}, NM_{high})
NM_{eff} = 40% of ½V_{DD}	**NM_{eff} = 80% of ½V_{DD}**	**NM_{eff} = -30% of ½V_{DD}**

Figure S6. How to determine the noise margin of an inverter from its transfer characteristics. The noise margin is a quantitative measure of the immunity of a logic circuit against electronic noise which is always present in electronic systems. The noise margin is the maximum voltage by which the input signal is allowed to fluctuate due to noise in either positive or negative direction without causing an erroneous output response. The parameters $V_{out,high}$, $V_{in,low}$, $V_{out,low}$ and $V_{in,high}$, from which the noise margin is calculated, are the side lengths of the two rectangles constructed as shown above. One corner of each rectangle is the point at which the slope of the V_{out}/V_{in} curve is -1, the other corner is the point (0,0) or (V_{DD},V_{DD}). Note that for the inverter based on a SAM of 100% fluoroalkyl phosphonic acid (right), a negative noise margin is obtained, which means that this inverter cannot be operated with a supply voltage of 2 V.

Table S1. Summary of the device and circuit parameters. Experimentally determined electrical parameters of pentacene p-channel TFTs, $F_{16}CuPc$ n-channel TFTs, complementary inverters, and complementary ring oscillators based on a SAM of 100% alkyl phosphonic acid, on a mixed SAM of 50% alkyl and 50% fluoroalkyl phosphonic acid, and on a SAM of 100% fluoroalkyl phosphonic acid.

		100% alkyl SAM	**50% alkyl + 50% fluoroalkyl SAM**	**100% fluoroalkyl SAM**
pentacene p-channel TFTs	**hole mobility**	0.75 cm^2/Vs	0.68 cm^2/Vs	0.25 cm^2/Vs
	threshold voltage	-1.1 V	-0.7 V	0.5 V
	on/off ratio	10^6	10^7	10^5
	subthreshold swing	150 mV/decade	120 mV/decade	250 mV/decade
$F_{16}CuPc$ n-channel TFTs	**electron mobility**	0.028 cm^2/Vs	0.027 cm^2/Vs	0.011 cm^2/Vs
	threshold voltage	-0.3 V	0.6 V	1.1 V
	on/off ratio	10^5	10^5	10^5
	subthreshold swing	160 mV/decade	180 mV/decade	220 mV/decade
compl. inverter	**switching voltage**	0.56 V	1.03 V	2.03 V
	maximum small-signal gain	78	430	50
	noise margin	40% of ½V_{DD}	80% of ½V_{DD}	-30% of ½V_{DD}
compl. ring oscillator	**smallest supply voltage**	1.5 V	1.0 V	2.5 V