Chemical Vapor Deposition of Graphene on a “Peeled-Off” Epitaxial Cu(111) Foil: A Simple Approach to Improved Properties

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ABSTRACT We present a simple approach to improving the quality of CVD grown graphene, exploiting a Cu(111) foil catalyst. The catalyst is epitaxially grown by evaporation on a single crystal sapphire substrate, thickened by electroplating, and peeled off. The exposed surface is atomically flat, easily reduced, and exclusively of (111) orientation. Graphene grown on this catalyst under atmospheric CVD conditions and without wet chemical prereduction produces single crystal domain sizes of several hundred micrometers in samples that are many centimeters in size. The graphene produced in this way can easily be transferred to other substrates using well-established techniques. We report mobilities extracted using field-effect (as high as 29 000 cm² V⁻¹ s⁻¹) and Hall bar measurement (up to 10 100 cm² V⁻¹ s⁻¹).

KEYWORDS: chemical vapor deposition · copper catalyst · graphene · epitaxial Cu (111) foil · peel-off · carrier mobility

Graphene is commonly synthesized by chemical vapor deposition (CVD) on copper (Cu) foils, a method favored for large-scale production. Unfortunately, the physical properties of graphene produced in this way typically do not reach those of exfoliated graphene. CVD graphene typically exhibits a high density of crystalline defects associated with grain boundaries as well as chemical impurities or defects. These defects result from surface chemical interactions with the polycrystalline Cu catalyst occurring during the CVD process. Problems associated with present Cu foil catalysts include the following: poly crystallinity of the catalyst surface (the (111) surface is most desirable for graphene CVD), multiple domain formation during CVD, surface roughness as well as chemical impurity arising from copper oxidation.

Graphene grown on an epitaxial Cu(111) film has been reported by several groups in attempts to produce samples with higher carrier mobility. Here, a mother substrate of C-plane sapphire is used to produce an epitaxial Cu film, typically ~200 nm in thickness, and the graphene is grown on top of the copper. The surface of such epitaxially grown Cu(111) films is not atomically flat. Because it is so thin, it is also fragile; for example, the removal of the copper from the sapphire/copper/graphene sandwich structure is difficult. The catalytic surface is also exposed, making it vulnerable to uncontrolled oxidation. Up to now, this approach has resulted in graphene properties with best carrier mobility of 2500 cm² V⁻¹ s⁻¹.

We have found a simple way to prepare an epitaxial copper foil on C-plane sapphire such that it can be subsequently peeled away. The peeling process exposes a fully intact Cu(111) crystalline surface, which is chemically clean and atomically flat. The sapphire substrate can be reused;
our approach effectively offers a practical means of fabricating (111) single crystalline catalyst surfaces for graphene growth. The graphene grown with this catalyst exhibits superior carrier mobility: up to 29,000 cm² V⁻¹ s⁻¹ measured by the field effect method and up to 10,100 cm² V⁻¹ s⁻¹ measured by the Hall bar method. Graphene domain sizes are typically several hundreds of μm and are stitched together in a continuous sample many cm's in size. The resulting graphene layers can easily be transferred to other substrates using well-established polymer coating and Cu etching techniques. The approach can be scaled to produce improved quality graphene sheets with several tens of centimeters in diameter.

RESULTS AND DISCUSSION

Figure 1a is a schematic illustration of the steps in the fabrication of graphene used in this work. First, an epitaxial Cu(111) film is grown on C-plane sapphire by evaporation to a thickness of several tens of nanometers. Subsequently, the film is thickened to several tens of micrometers by electrochemical means, at which point it is easily peeled away from the sapphire substrate. The side of the foil originally in contact with the sapphire is then used in catalytic CVD graphene growth similar to previous reports.

C-plane sapphire is an excellent choice as mother substrate since (i) it has a suitable (8.6%) lattice mismatch with Cu(111), sufficiently close to the Cu(111) lattice to allow epitaxial growth and sufficiently different to provide needed stress for easy in peel-off; (ii) it is a near perfect insulator and therefore compatible with electrochemistry; (iii) it has excellent and manipulable interface properties, which leads to easy peel-off of the metallic Cu; (iv) it has a relatively low cost; and (v) it can be produced in sizes up to 15 in. Furthermore, the mother substrate can be continuously reused.

The 50 nm thick epitaxial Cu film formed on C-plane sapphire shows exclusive (111) orientation prior to peel-off—see X-ray diffraction data in Figure 1b. The Cu–Cu distance between surface atoms in copper (111) is 0.255 nm, while the O–O surface atom distance in C-plane sapphire is 0.279 nm. The (8.6%) atomic spacing mismatch allows for pseudoepitaxial grown of Cu(111) on C-plane sapphire. We attribute the 6-fold azimuthal symmetry seen in X-ray diffraction to symmetry breaking of the 3-fold (111) surface pattern associated with fcc Cu when epitaxially grown on C-plane sapphire; the Moiré pattern of the interface has a 6-fold symmetry, even though both interfaces have 3-fold symmetry. See Supporting Information Figure S1.
The compressive stress between the Cu film and C-plane sapphire combined with the weak adhesion between the two materials allows the Cu film to be peeled from the substrate easily (Figure 1c). The side of the Cu foil that has been peeled away from the sapphire is exceedingly flat. Figure 1d shows flatness over a wide area as measured by a surface profiler with 20 nm resolution. The roughness of the peeled-off surface cannot be seen with this method even over the 1 mm area shown. By contrast, the opposite surface of the film electrochemically thickened and peeled off, shows a RMS roughness of 740 nm. Figure 1e shows higher resolution roughness measurements using air AFM over a smaller (1 μm square) area. The peeled-off Cu surface exhibits an RMS roughness of 0.40 nm. For comparison, the surface roughness of the opposing surface exhibits an RMS roughness of 0.86 nm before electrochemical thickening and 7.1 nm after.

We observed no damage to the Cu crystalline structure after peel-off, using electron backscatter diffraction (EBSD); however, two azimuthal orientation domains are formed. Figure 2a,b shows EBSD results revealing two kinds of 3-fold symmetric domains, indicated as orange and green (see also Supporting Information Figure S2). The two diffractions result from different azimuthal orientations of the Cu(111) surface. This may be due to an epitaxial twinning at the copper-sapphire interface, possibly resulting from peel-off. We estimate the size of the azimuthal surface domains shown in Figure 2a to be ∼23 μm for the orange area and ∼122.7 μm for the green area although this not much larger than the spatial resolution limit of our EBSD instrument.

The oxidation of Cu surfaces, forming CuO, Cu₂O, and Cu(OH)₂, is hard to avoid under ambient conditions since copper oxides are thermodynamically favored. CVD using conventional copper foil catalysts normally involves removal of the native oxide prior to graphene growth, for example by treatment with acetic acid or (NH₄)₂S₂O₈. By contrast, the peeled-off catalyst surface is formed in a chemically pure state and only begins to oxidize slowly upon exposure to air. Figure 2c shows X-ray photoemission spectra (XPS) in the Cu 2p region for several Cu foil surfaces. Ordinary Cu foil yields a strong oxide signal in XPS compared to signals reflecting Cu–Cu bonding. By contrast, the peeled-off surface, stored in air for 1 week prior to analysis, shows only limited oxide formation. When the conventional copper foil is treated with (NH₄)₂S₂O₈ solution for 1 min, XPS still shows substantially more oxide compared to the untreated peeled-off Cu foil. See also the O 1s spectra in Supporting Information Figure S3.
We now turn to a discussion of the graphene produced with the peeled-off catalyst. For comparison, we also prepared graphene on normal Cu foil (Alfa Aesar, item No. 13382, 99.8% purity) according to literature protocols1 and on thin epitaxial films (See Supporting Information discussing Figure S4). The normal Cu foil required us to follow the temperature–time curve shown in Figure 3a, which included a substantial pre-annealing time of the Cu foil in a hydrogen reducing atmosphere at 1000 °C (step S2). Reductive annealing is essential to the success of CVD graphene growth using normal copper foil, both to remove residual oxide and to help form larger (111) crystalline domains. Even with reductive annealing, some copper oxides remain on the catalyst; these were identified as segregated CuO nanoparticles by area resolved energy dispersed X-ray spectroscopy (EDX). See Supporting Information Figure S5a. These copper oxide nanoparticles degrade the quality of the CVD graphene and complicate transfer and removal. Wet chemical reduction methods have been employed to help ameliorate this well-known problem; for example, pretreatment in acetic21 or hydrochloric acid23 (see Supporting Information Figure S5b). Alternatively, electro-mechanical polishing to remove oxide has been reported.24,25 We found that etching with (NH₄)₂S₂O₈ gave better results than any of these other methods. We etched normal Cu foil for the reference graphene growth by dipping in (NH₄)₂S₂O₈ solution for 1 min before CVD growth. This led to the best removal of Cu oxide nanoparticles (Supporting Information Figure S6) and the highest quality reference graphene.

Graphene films were synthesized from peeled-off epitaxial Cu(111) foil using the CVD method with methane as the carbon source and hydrogen as carrier gas. No wet chemical reduction or high temperature reductive pre-annealing was employed with this catalyst. Instead, the simplest possible temperature–time curve was used; see Figure 3e. The graphene from the peeled-off catalyst exhibits higher crystalline quality than that produced on normal Cu foil. The domain size distribution can be probed by coating the graphene with a nematic liquid crystal (4-pentyl-4′-cyanobiphenyl (5CB)) and examining the sample with polarized optical microscopy.26 These images are shown in Figure 3b (for normal foil catalyst) and Figure 3f (for peeled-off catalyst). By rotating the polarizer in the optical microscope by 90°, we visualize the domain size of the graphene samples.26 For reference graphene, it is difficult to identify grain boundaries (Figure 3b), indicating that the grain size is close to the optical diffraction limit. By contrast, graphene from the peeled-off catalyst shows distinct grain boundaries for the copper catalyst (dark solid lines), as well distinct graphene domains, which are sensitive to the polarization angle. EBSD analysis of the peeled-off catalyst after CVD growth still shows large (111) domains; however, the rotational orientation of the...
domains is now more widely distributed (see Supporting Information Figure S7). The graphene domain-size is as large as 100 μm (Figure 3f). The small grain size of the reference graphene is confirmed by transmission electron microscopy (Figures 3c,d). Here, an area with 1 μm diameter is analyzed. The electron diffraction pattern shows 12 spots, indicating two crystalline orientations within the field of view. The dark-field image (Figure 3d) derived from the circled region in the inset of Figure 3d shows that the domain size is on the order of 0.5 μm or less, consistent with our polarimetry results. 6-fold symmetric electron diffraction patterns are obtained for graphene produced on peeled-off Cu foil (Figure 3g,h insets). The diffraction spots of several areas reveal no grain boundaries on the 1 μm length scale, a result also consistent with our polarimetry.

We conclude that the domain size of graphene obtained from a peeled-off catalyst is about 2 orders of magnitude larger than that obtained from a normal copper foil. It is likely that the large graphene domain size obtained from peeled-off catalyst reflects a smaller density of nucleation sites for graphene crystal growth on this nearly ideal copper catalyst.27 This suggests that further improvements in catalyst preparation might lead to yet larger average domain sizes, a topic of ongoing effort in our laboratory.

Figure 4a shows Raman spectra of both peeled-off and reference graphene. The Raman spectrum shows typical features associated with monolayer graphene for both samples, namely an intensity ratio of the 2D and G lines between 2 and 3 as well as a symmetric 2D band with a fwhm of 36.0 cm⁻¹ for reference graphene and 32.6 cm⁻¹ for graphene from peeled-off Cu foil.28 Moreover, the graphene grown on peeled-off Cu foil has no detectable D band. By contrast, we observe a weak D band for reference graphene.

We measured charge carrier mobilities of the graphene devices in liquid using field-effect and Hall bar configurations. The graphene sheets are transferred onto Si/SiO₂ chips with prepatterned electrodes. A schematic of the field-effect transistor configuration is shown in Figure 4b.29 The Si/SiO₂ chip along with the electrodes and the graphene sheet are brought in contact with a droplet of water containing 10 mM KCl. A Ag/AgCl reference electrode also immersed in the droplet acts as the gate. The resistance of the graphene sheet across the source (S)/drain (D) electrodes (typical electrode spacing: 3 μm) is measured as a function of the voltage applied to the gate electrode, V_GATE. In this configuration, the electrical double layer at the graphene/liquid interface serves as the gate capacitor.29 Figure 4c shows the measured resistance as a function of gate voltage, V_GATE, for both reference and peeled-off graphene devices. Several features immediately distinguish the transport characteristics of the two samples. The graphene synthesized on peeled-off Cu foil exhibits a narrow resistivity peak (fwhm is ~0.28 V), with the charge neutrality point (CNP) very close to zero (V_GATE = 0.027 V). For reference graphene, the fwhm is 0.44 V and the CNP is 0.18 V. Furthermore, the
peeled-off graphene exhibits low resistance. Charge carrier mobility is derived from a modified Drude model, which is fitted to the resistance measurements shown as the red curves in Figure 4c (see Supporting Information Figure S8). The charge carrier mobility of peeled-off graphene was found to be as high as 29,000 cm² V⁻¹ s⁻¹. These features indicate a higher degree of order and reduced impurity doping in the peeled-off graphene. This may be partly due to the improved order of the catalyst surface. We used AFM to examine the catalyst surface after CVD growth. See Supporting Information Figure S9. The step-terrace structure of the peeled-off Cu surface shows parallel aligned steps. For normal Cu foil, the steps are rounded. This may mean that the step-terrace structure of peeled-off Cu offers an advantage to carrier mobility as nanoripples are expected to likewise be parallel aligned; hence mobility in certain directions may be improved. We note that this value approaches the upper limit of charge carrier mobility that can be obtained with this method. Similar mobility values were obtained on other samples, with values in the range of 8000–30,000 cm² V⁻¹ s⁻¹ for peeled-off graphene in comparison to 1500–10,000 cm² V⁻¹ s⁻¹ for reference graphene.

To gather further evidence of the high carrier mobility of the peeled-off graphene, we also performed Hall bar measurements in liquid. Figure 5 collects the 4-probe resistance, mobility and charge carrier concentration as a function of the applied gate voltage. It is apparent that for this sample, we observe a charge carrier mobility of around 6500 cm² V⁻¹ s⁻¹ at a carrier concentration of 4.2 × 10¹⁰ cm⁻² for both holes and electrons. Similar values in the range of 5000 to 10,000 cm² V⁻¹ s⁻¹ are obtained for other samples with the highest observed mobility of around 10,100 cm² V⁻¹ s⁻¹ at a carrier concentration of 4.7 × 10¹⁰ cm⁻² (see Supporting Information Figure S10). However, in these devices, the transport is asymmetric with electron mobilities much higher than that of holes. The mobility values observed in the Hall measurements are lower than that of the field-effect mobilities. This may be attributed partly to the larger sizes of the structures here (length and width of central Hall bar is 10 μm × 5 μm) in comparison to the field-effect devices. It is also worth pointing out that the lowest carrier densities that we can attain using liquid gating are much higher than the concentration values typically reported for back-gated devices. Most likely, the presence of the liquid/ionic background along with the use of SiO₂ substrate limits the performance of our devices to these mobility values.

CONCLUSIONS

To summarize and conclude, we report a novel CVD graphene synthesis, which relies on a near perfect (111) copper surface catalyst produced by epitaxial growth on C-plane sapphire and a simple peel-off procedure. The “peel-off” catalyst produces CVD graphene, which exhibits electronic performance comparable to that of exfoliated graphene, with domain sizes larger than 100 μm. The sapphire mother substrate can be used over and over to produce additional copper foil catalyst. As a result of progress in crystal growth technology, one can realistically speculate that this peel-off approach can be scaled to produce graphene sheets several tens of centimeters in size. This peeling off approach can certainly also be applied to a number of other catalytically important metal foils such as nickel, cobalt, iron, ruthenium, palladium, iridium, platinum, and their alloys. These results provide new opportunities for complementary technological advancements for high quality and large-area graphene devices.

METHODS

Preparation of Cu Foil. C-plane sapphire (double side polished, Crystal Bank Research Institute in Pusan National University, Korea) was used as a mother substrate. After Cu films were cleaned sequentially with acetone, isopropyl alcohol, and deionized water, they were deposited by electron beam deposition using a high purity Cu source from Sigma-Aldrich (item number: 254177, Cu beads, 2–8 mm, 99.9999% trace metals basis). The sapphire substrate was dipped in HCl + DI (volume ratio 1:1) during 1–2 min to remove surface contamination right before Cu deposition. The Cu films were grown to 50 nm thickness at a rate of 0.03 nm/c. The growth chamber pressure was
maintained at about 10\(^{-3}\) Torr during deposition, and the substrate was held at room temperature. For the Cu plating on epitaxial Cu film, the cathode (50 nm Cu film on C-plane sapphire) and anode (Bulk Cu stick) were electrically connected with a Keithley 2400 digital source meter for the constant current density (15 mA/cm\(^2\)). During electroplating, the voltage between electrodes varied between 0.2 and 0.3 V and the growth rate was about 16 \(\mu\)m/h and controlled to yield a final thickness of \(-25 \mu m\). The temperature of the electroplating solution was 60\(^\circ\)C. Due to large compressive stress between the thick Cu foil and the C-plane sapphire substrate, all Cu foils could easily be peeled-off from the sapphire substrate.

**Graphene Growth.** The peeled-off Cu foils were loaded into a quartz tube reaction chamber. A growth process is as follows. First, the pressure in the growth chamber is pumped down to 3 mTorr using a mechanical pump. Second, a 40 scm flow of hydrogen gas is introduced into the chamber at 950 mTorr. Third, the Cu foils were heated to 1000 \(\degree\)C over 60 min. Fourth, 6 scm flow of methane gas with 20 scm hydrogen is introduced into the chamber for 10 min with a total pressure of 460 mTorr for graphene synthesis; after growth, the furnace was cooled down rapidly to room temperature under a 20 scm flow of hydrogen.

For the reference graphene on normal Cu foil (Alfa Aesar, item No. 13382, 99.8% purity), the Cu foils were first immersed in 0.3 mol ammonium persulfate ((NH\(_4\))\(_2\)S\(_2\)O\(_8\), Sigma-Aldrich, item No. 248614, ACS reagent, (NH\(_4\))\(_2\)S\(_2\)O\(_8\) for the Cu etching; it is advantageous to use etching solutions, such as Fe(NO\(_3\))\(_3\) and FeCl\(_3\) solutions. A typical etching method is as follows: (1) Domain mismatch in C–Al\(_2\)O\(_3\) system, (2) Electrochemical deposition of Cu foils, (3) Copper Foils. (4) graphene growth on epitaxial Cu (111)/sapphire without plating and peeling off, (5) prereduction of normal Cu foil-removal of native oxide by acetic acid, (6) prereduction of normal Cu foil-removal of native oxide by ammonium persulfate, (7) BDS results of peeled-off Cu foil after graphene growth, (8) high carrier mobility derived from Drude model fit to field effect measurements, (9) step-terrace of Cu foils after graphene growth, (10) high carrier mobility derived from Hall bar measurements. This material is available free of charge via the Internet at http://pubs.acs.org.

**REFERENCES AND NOTES**
